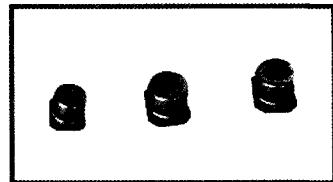


## V-CHIP ALUMINUM ELECTROLYTIC CAPACITORS 片式铝电解电容器

**LZ** Low Impedance  
Series

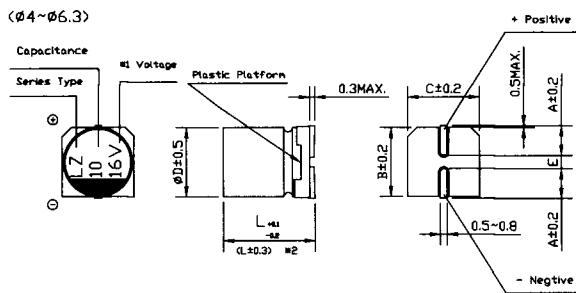


- Chip type, low impedance, temperature range up to +105°C.
- Designed for surface mounting on high density circuit board.
- Emboss carrier tape packing system is available for automatic insertion.

#### ◆ Specifications

Items	Performance Characteristics										
Operating Temperature Range	-55~+105°C										
Voltage Range	6.3~35V										
Capacitance Range	1~220μF										
Capacitance Tolerance	±20% at 120 Hz, 20°C										
Leakage Current	After 2 minutes' application of rated voltage, leakage current is not more than 0.01CV or 3μA, whichever is greater.										
Tan δ	Measurement frequency: 120Hz, Temperature: 20°C										
	Rated voltage(V)	6.3	10	16	25	35					
	Tan δ (max)	0.22	0.19	0.16	0.14	0.12					
Stability at Low Temperature	Measurement frequency: 120Hz										
	Rated voltage(V)	6.3	10	16	25	35					
	Impedance ratio	Z-25°C/Z+20°C	2	2	2	2					
	ZT/Z20(max)	Z-40°C/Z+20°C	4	4	3	3					
Load Life	After 1000 hours' application of rated voltage at 105°C, capacitors meet the characteristics requirements listed at right.										
	Capacitance Change	Within ± 20% of initial value									
	Tan δ	200% or less of initial specified value									
	Leakage Current	Initial specified value or less									
Self Life	After leaving capacitors under no load at 105°C for 1000 hours, they meet the specified value for load life characteristics listed above.										
Resistance to Soldering Heat	The capacitors shall be kept on the hot plate maintained at 250°C for 30 seconds. After removing from the hot plate and restored at room temperature, they meet the characteristics requirements listed at right.										
	Capacitance Change	Within ± 10% of initial value									
	Tan δ	Initial specified value or less									
	Leakage Current	Initial specified value or less									
Applicable Standards	JIS C-5141 and JIS C-5102										

#### ◆ Chip Type



ΦD × L	4 × 5.4	5 × 5.4	6.3 × 5.4	6.3 × 7.7
A	1.8	2.1	2.4	2.4
B	4.3	5.3	6.6	6.6
C	4.3	5.3	6.6	6.6
E	1.0	1.3	2.2	2.2
L	5.4	5.4	5.4	7.7

\*1 Voltage mark for 6.3V is [6V]

\*2 Applicable to 6.3×7.7

# V-CHIP ALUMINUM ELECTROLYTIC CAPACITORS 片式铝电解电容器

**LZ** Series

• Dimensions

Cap. ( $\mu\text{F}$ ) \ WV	6.3		10		16		25		35		
	QJ	1A	1C	1E	1V						
1.0 010									4x5.4	5.0	50
1.5 1R5									4x5.4	5.0	50
2.2 2R2									4x5.4	5.0	50
3.3 3R3									4x5.4	5.0	50
4.7 4R7							4x5.4	5.0	4x5.4	5.0	50
6.8 6R8							4x5.4	5.0	5x5.4	2.6	80
10 100				4x5.4	5.0	50	5x5.4	2.6	80	5x5.4	2.6
15 150				5x5.4	2.6	80	6.3x5.4	1.3	115	6.3x5.4	1.3
22 220	4x5.4	5.0	50	5x5.4	2.6	80	6.3x5.4	1.3	115	6.3x5.4	1.3
33 330	5x5.4	2.6	80	5x5.4	2.6	80	6.3x5.4	1.3	115	6.3x7.7	0.8
47 470	5x5.4	2.6	80	6.3x5.4	1.3	115	6.3x5.4	1.3	115	6.3x7.7	0.8
68 680	6.3x5.4	1.3	115	6.3x5.4	1.3	115	6.3x7.7	0.8	150		
100 101	6.3x5.4	1.3	115	6.3x7.7	0.8	150	6.3x7.7	0.8	150		
150 151	6.3x7.7	0.8	150	6.3x7.7	0.8	150					
220 221	6.3x7.7	0.8	150						Case size	Impedance	Allowable ripple

Maximum impedance ( $\Omega$ ) at 20°C 100kHz, allowable ripple (mA rms) at 105°C 100kHz

• Frequency coefficient of allowable ripple current

Frequency	50Hz	120Hz	300Hz	1kHz	10kHz~
Coefficient	0.35	0.50	0.64	0.83	1.00